

Layer Stack Up Detail for: SU601147B.PcbDoc

Layer	Dielectric	Copper	Dielectric	Dielectric	Dielectric
Top Solder Mask	(.015)	Thickness	Height	Material	Type
Top Layer	(.015)	1.4mil	0.4mil	Solder Resist	
Middle Layer 1	(.015)	1.4mil	12.6mil	FR-4	Core
Middle Layer 2	(.02)	1.4mil	30.4mil	FR-4	Prepreg
Bottom Layer	(.015)	1.4mil	12.6mil	FR-4	Core
Bottom Solder Mask	(.015)	0.4mil		Solder Resist	

DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)

2760MIL X 1865MIL

Number of Layers : 4

MIN. TRACK WIDTH: 4 MIL

MIN. CLEARANCE: 4 MIL

MIN. VIA PAD SIZE: 20 MIL

MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL

PER IPC-D-275 CLASS 2 LEVEL C

REGISTRATION TOLERANCES: METAL +/- .5 MIL, HOLES +/- .3 MIL

MATERIAL:

FR-408 ☒ FR-4 High Tg ☐ OTHER

THICKNESS: ☒ 62 MIL (1.6mm) +/-10% ☐ OTHER

TOLERANCE: ☒ ANSI IPC-6012 TYPE 3 CLASS 2

OTHER +/-

BOW & TWIST: ☒ ANSI IPC-6012 TYPE 3 CLASS 2

OTHER +/-

COPPER THICKNESS (FINISHED):

OUTER: ☒ 1.4MIL (1oz) ☐ 2MIL (1.4oz) ☐ 2.8MIL (2oz)

INNER SIGNAL: ☒ 1.4MIL (1oz) ☐ 2.8MIL (2oz) ☐ N/A

DRILLING:

REFERENCE: ☒ AS SHOWN ☒ NC DRILL FILES

PTH MIN COPPER THICKNESS: ☒ 1MIL ☐ OTHER

BOARD FINISH:

SILKSCREEN: ☒ TOP ☒ BOTTOM

SILKSCREEN COLOR: ☒ WHITE ☐ OTHER

SOLDER RESIST COLOR:

☒ GREEN ☐ BLUE ☐ OTHER

SURFACE FINISH: ☒ IMMERSION GOLD (ENG) ☐ ENIG

☐ IMM. TIN/SILVER OR EQUIV ☐ OTHER

ARRAY/PANEL: ☐ CUT AND TRIM PER MECH LAYER 1

☒ N.C. ROUTE ☐ V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:

☒ ANSI IPC-A-600F CLASS -> ☐ 1 ☒ 2 ☐ 3

☒ UL 94V-0 ☒ RoHS ☐ OTHER PER ORDER

ADDITIONAL REQUIREMENTS:

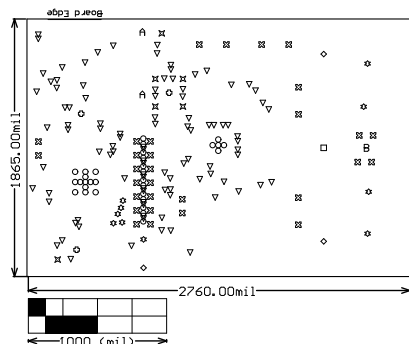
MICROSECTION: ☐ YES

BARE BOARD ELEC. TEST: ☐ NONE ☒ REQUIRED ☐ PER ORDER

MANUFACTURER'S UL: ☐ RAL ☐ METAL ☒ SILK

Symbol	Hit Count	Tool Size	Physical Length	Route Path Length	Plated	Hole Type
o	18	7.874mil (0.2mm)			PTH	Round
v	87	8mil (0.203mm)			PTH	Round
v	12	14mil (0.356mm)			NPTH	Round
o	9	15mil (0.381mm)			PTH	Round
o	19	20mil (0.508mm)			NPTH	Round
o	2	25mil (0.635mm)			PTH	Round
x	2	28mil (0.711mm)			PTH	Round
#	22	40mil (1.016mm)			PTH	Round
#	8	51.18mil (1.3mm)			PTH	Round
o	1	59.055mil (1.5mm)			PTH	Round
x	4	66.929mil (1.7mm)			PTH	Round
o	1	31mil (0.787mm)	162mil (4.115mm)	131mil (3.327mm)	NPTH	Slot
e	1	31mil (0.787mm)	200mil (5.08mm)	169mil (4.293mm)	NPTH	Slot
a	1	31mil (0.787mm)	250mil (6.35mm)	215mil (5.563mm)	NPTH	Slot
o	1	31mil (0.787mm)	460mil (11.681mm)	425mil (10.807mm)	NPTH	Slot
B	1	31mil (0.787mm)	540mil (13.716mm)	509mil (12.925mm)	NPTH	Slot
A	1	31mil (0.787mm)	600mil (15.24mm)	569mil (14.455mm)	NPTH	Slot
D	1	31mil (0.787mm)	810mil (20.574mm)	773mil (19.787mm)	NPTH	Slot
186 Total						

Slot definitions : Route Path Length = Calculated from tool start centre position to tool end centre position.
Physical Length = Route Path Length + Tool Size = Slot length as defined in the PCB layout



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601126	REV: B	SUN REV: Not In VersionControl	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.	ENGINEER: Ben Kasemsadeh	LAYOUT BY: Ben Kasemsadeh
LAYER NAME = 060850-001					FILE NAME: SU601126B.PcbDoc	ALUM DESIGNER VERSION: 14.3.16.37051
PLOT NAME = Fabrication Drawing	GENERATED : 3/5/2015 5:45:46 PM	TEXAS INSTRUMENTS		SCALE: 1.00		